

Assoc. Prof. ANIL ÇELEBİ

Personal Information

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International Researcher IDs

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Publons / Web Of Science ResearcherID: P-6118-2019

ScopusID: 36793379200

Yoksis Researcher ID: 154061

Education Information

Post Doctorate, Seoul National University , College of Engineering, Electrical and Computer Engineering, South Korea 2009 - 2009

Doctorate, Kocaeli University, Fen Bilimleri Enstitüsü, Electronics and Telecommunications Engineering, Turkey 2005 - 2008

Postgraduate, Kocaeli University, Fen Bilimleri Enstitüsü, Electronics and Telecommunications Engineering, Turkey 2002 - 2005

Undergraduate, Kocaeli University, Mühendislik Fakültesi, Elektronik ve Haberleşme Mühendisliği, Turkey 1998 - 2002

Biography

I work as a doctoral lecturer in Kocaeli University Electronics and Communication Engineering Department and manage R&D activities at Kuantek Elektronik ve Bilişim San.Tic.Ltd.Şti. ([Wwww.kuantek.com.tr](http://www.kuantek.com.tr)). As a lecturer, I teach undergraduate, graduate and doctoral students as an academic consultant, as well as lectures on digital design, digital design with FPGA, and embedded system design with FPGA. I provide algorithm development, hardware design, training and consultancy services in signal and image processing within the body of Kuantek Elektronik. In this context, I work on video signal processing systems, acoustic signal processing systems, IoT systems. JESD204B IP core, CAN FD IP core, Day TV ISP FPGA design, thermal ISP FPGA design, PCIe root complex module, automotive peripheral vision system FPGA design are some of the topics I have been working on.

Between 2008-2013, Pars Ar-Ge ve Bilgi Teknolojileri Ltd. Şti., I managed the hardware-related R&D activities as a board member.

Between 2009-2010, within the scope of compulsory public service, he ranks second lieutenant and dr. I worked as an instructor at the Military Academy. In this context, I gave courses on digital design, electronics and microcontrollers to cadets.

Kocaeli University in Turkey conducted joint Chung-Ang University and Seoul National University in South Korea have worked as a researcher at the two international research projects. I also worked as a postdoctoral research fellow at the Computer Architecture and Parallel Processing Laboratory at Seoul National University under the Korean government's Brain Korea 21 program.

During my PhD education, I worked on the algorithm and hardware architecture design of high speed motion estimation for H.264 / AVC. During my doctoral studies, I have published articles and notices in various journals and conferences.

During my graduate education, I worked on the design of analog and digital electronic circuits. In this context, I have been a researcher and scholarship in scientific research projects involving the realization and production of analog / digital converters with VLSI technology.

Foreign Languages

English, C1 Advanced

Certificates, Courses and Trainings

Entrepreneurship, Berkeley - T-Jump Accelerator, University of California, Berkeley, 2017

Vocational Course, HDL Designer Live Online Training, Mentor Graphics, 2015

Vocational Course, SystemVerilog For Verification Training, Mentor Graphics, 2014

Vocational Course, Essential Verification with SystemVerilog and UVM, IMEC, 2014

Dissertations

Doctorate, H.264/AVC'de hızlı hareket kestirimi için düşük güçlü donanım mimarileri ve algoritma ortak tasarımı, Kocaeli University, Fen Bilimleri Enstitüsü, Electronics and Telecommunications Engineering, 2008

Postgraduate, 10 bit iki adımlı analog sayısal dönüştürücünün VLSI tasarımı ve benzetimi, Kocaeli University, Fen Bilimleri Enstitüsü, Fen Bilimleri Enstitüsü, 2005

Research Areas

Equipment, Processor Architectures, Logic Design, Integrated Circuits, Electrical and Electronics Engineering, Printed Circuits, Thin Film, Thick Film and Hybrid ICs, Sensing Devices and Transducers, Electronic Circuits

Academic Titles / Tasks

Associate Professor, Kocaeli University, Mühendislik Fakültesi, Elektronik ve Haberleşme Mühendisliği, 2021 - Continues

Assistant Professor, Kocaeli University, Mühendislik Fakültesi, Elektronik ve Haberleşme Mühendisliği, 2009 - 2021

Lecturer PhD, Land War Academy Command, Turkish Military Academy (Dean's Office), 2009 - 2010

Research Assistant, Kocaeli University, Mühendislik Fakültesi, Elektronik ve Haberleşme Mühendisliği, 2002 - 2009

Courses

Tümleşik Sayısal Devrelerin Fonksiyonel Olarak Doğrulanması, Postgraduate, 2021 - 2022, 2020 - 2021, 2019 - 2020,

2018 - 2019, 2015 - 2016, 2014 - 2015

İleri Sayısal Tasarım, Postgraduate, 2021 - 2022, 2020 - 2021, 2018 - 2019, 2017 - 2018, 2016 - 2017, 2015 - 2016, 2013 - 2014, 2012 - 2013, 2011 - 2012

Fonksiyonel Doğrulamaya Giriş, Undergraduate, 2021 - 2022, 2020 - 2021, 2019 - 2020, 2018 - 2019, 2015 - 2016

FPGA'lar ile Sayısal Tasarıma Giriş, Undergraduate, 2021 - 2022, 2020 - 2021, 2018 - 2019, 2017 - 2018, 2016 - 2017

Bilgisayar Donanım Mimarisi, Postgraduate, 2021 - 2022, 2020 - 2021, 2019 - 2020, 2018 - 2019, 2017 - 2018, 2016 - 2017, 2015 - 2016

Digital Design, Undergraduate, 2021 - 2022, 2020 - 2021, 2019 - 2020, 2018 - 2019, 2017 - 2018, 2016 - 2017, 2015 - 2016, 2014 - 2015, 2013 - 2014, 2012 - 2013, 2011 - 2012, 2010 - 2011

Mühendislik Tasarımı - 3, Undergraduate, 2016 - 2017

Mühendislik Uygulamalarına Giriş, Undergraduate, 2016 - 2017

Mikroişlemciler, Undergraduate, 2016 - 2017

Mühendislik Tasarımı - 2, Undergraduate, 2016 - 2017

Mühendislik Tasarımı - 1, Undergraduate, 2016 - 2017

Elektronik Laboratuvarı I, Undergraduate, 2016 - 2017, 2015 - 2016, 2014 - 2015, 2013 - 2014, 2012 - 2013, 2011 - 2012, 2010 - 2011

Elektronik II, Undergraduate, 2014 - 2015, 2013 - 2014, 2010 - 2011

Elektronics I, Undergraduate, 2015 - 2016, 2014 - 2015, 2011 - 2012, 2010 - 2011

Elektronik Laboratuvarı II, Undergraduate, 2010 - 2011

Advising Theses

Çelebi A., Düşük karmaşıklığa sahip hareket kestirimi donanım mimarileri, Postgraduate, S.ÇETİN(Student), 2019

Çelebi A., HEVC CABAC için ikileştirme modülü donanım mimarisi, Postgraduate, Y.ÇETİN(Student), 2018

Çelebi A., Uyarlamalı saat eş-zamanlama yönteminin FPGA kullanılarak gerçekleştirilmesi, Postgraduate, S.DEMİR(Student), 2018

Çelebi A., H.264/AVC ve HEVC için BİT düzlemi uyumlama temelli hareket kestirimi ve donanım mimarisi, Postgraduate, M.ASLAM(Student), 2017

Çelebi A., Yanıcı patlayıcı ortamlarda kullanılabilir bir elektronik sistem tasarımı, Postgraduate, S.İRKİT(Student), 2016

Designed Lessons

Çelebi A., FPGAlar ile Gömülü Sistem Tasarımı, Undergraduate, 2018 - 2019

Çelebi A., Bilgisayar Donanım Mimarisi, Doctorate, 2015 - 2016

Çelebi A., Fonksiyonel Doğrulamaya Giriş, Undergraduate, 2015 - 2016

Çelebi A., Introduction to Digital Design with FPGAs, Undergraduate, 2015 - 2016

Çelebi A., Tümlşik Sayısal Devrelerin Fonksiyonel Olarak Doğrulanması, Doctorate, 2013 - 2014

Çelebi A., Verilog HDL ile Sayısal Tasarım, Undergraduate, 2011 - 2012

Çelebi A., Sayısal Tasarım, Undergraduate, 2010 - 2011

Çelebi A., İleri Sayısal Tasarım, Doctorate, 2010 - 2011

Çelebi A., FPGA ile İşaret İşleme Sistemleri Tasarımına Giriş, Undergraduate, 2010 - 2011

Designed Programs

Çelebi A., Undergraduate, National Defence University Turkish Military Academy Electronics Engineering Department, 2011 - Continues

Published journal articles indexed by SCI, SSCI, and AHCI

- I. **FPGA based real-time epileptic seizure prediction system**
Cosgun E., ÇELEBİ A.
BIOCYBERNETICS AND BIOMEDICAL ENGINEERING, vol.41, no.1, pp.278-292, 2021 (SCI-Expanded)
- II. **Selective Gray-Coded Bit-Plane-Based Two-Bit Transform and Its Efficient Hardware Architecture for Low-Complexity Motion Estimation**
CELEBI A., YAVUZ S., ÇELEBİ A., URHAN O.
IEEE TRANSACTIONS ON CONSUMER ELECTRONICS, vol.64, no.3, pp.259-266, 2018 (SCI-Expanded)
- III. **One-Dimensional Filtering Based Two-Bit Transform and Its Efficient Hardware Architecture for Fast Motion Estimation**
CELEBI A., YAVUZ S., ÇELEBİ A., URHAN O.
IEEE TRANSACTIONS ON CONSUMER ELECTRONICS, vol.63, no.4, pp.377-385, 2017 (SCI-Expanded)
- IV. **A General Digit-Serial Architecture for Montgomery Modular Multiplication**
ERDEM S. S., YANIK T., ÇELEBİ A.
IEEE TRANSACTIONS ON VERY LARGE SCALE INTEGRATION (VLSI) SYSTEMS, vol.25, no.5, pp.1658-1668, 2017 (SCI-Expanded)
- V. **Hierarchical Order Statistics Filtering for Fast Bi-Dimensional Empirical Mode Decomposition**
Semiz S., ÇELEBİ A., URHAN O.
ETRI JOURNAL, vol.38, no.4, pp.695-702, 2016 (SCI-Expanded)
- VI. **Selective Gray-Coded Bit-Plane Based Low-Complexity Motion Estimation and its Hardware Architecture**
YAVUZ S., ÇELEBİ A., ASLAM M., URHAN O.
IEEE TRANSACTIONS ON CONSUMER ELECTRONICS, vol.62, no.1, pp.76-84, 2016 (SCI-Expanded)
- VII. **Hierarchical Order Statistics Filtering for Fast Bi dimensional EMD**
SEMİZ S., ÇELEBİ A., URHAN O.
Etri Journal, vol.38, 2016 (SCI-Expanded)
- VIII. **Wireless sensor network-based extension to KNX home automation system**
GOKBAYRAK A. B., KILIVAN S., AKIN S., ÇELEBİ A., URHAN O.
TURKISH JOURNAL OF ELECTRICAL ENGINEERING AND COMPUTER SCIENCES, vol.24, no.5, pp.3652-3663, 2016 (SCI-Expanded)
- IX. **Bit Plane Matching Based Variable Block Size Motion Estimation Method and Its Hardware Architecture**
ÇELEBİ A., Lee H., Ertuerk S.
IEEE TRANSACTIONS ON CONSUMER ELECTRONICS, vol.56, no.3, pp.1625-1633, 2010 (SCI-Expanded)
- X. **Truncated Gray-Coded Bit-Plane Matching Based Motion Estimation and its Hardware Architecture**
ÇELEBİ A., AKBULUT O., URHAN O., Ertuerk S.
IEEE TRANSACTIONS ON CONSUMER ELECTRONICS, vol.55, no.3, pp.1530-1536, 2009 (SCI-Expanded)
- XI. **Efficient Hardware Implementations of Low Bit Depth Motion Estimation Algorithms**
ÇELEBİ A., URHAN O., Hamzaoglu I., ERTÜRK S.
IEEE SIGNAL PROCESSING LETTERS, vol.16, no.6, pp.513-516, 2009 (SCI-Expanded)
- XII. **A Low-Complexity Approach for the Color Display of Hyperspectral Remote-Sensing Images Using One-Bit-Transform-Based Band Selection**
Demir B., ÇELEBİ A., Ertuerk S.
IEEE TRANSACTIONS ON GEOSCIENCE AND REMOTE SENSING, vol.47, no.1, pp.97-105, 2009 (SCI-Expanded)
- XIII. **An All Binary Sub-Pixel Motion Estimation Approach and its Hardware Architecture**
ÇELEBİ A., AKBULUT O., URHAN O., Hamzaoglu I., ERTUERK S.
IEEE TRANSACTIONS ON CONSUMER ELECTRONICS, vol.54, no.4, pp.1928-1937, 2008 (SCI-Expanded)

Articles Published in Other Journals

- I. **Hardware Implementation of Local Binary Pattern Based Two-Bit Transform Motion Estimation**
Yavuz S., ÇELEBİ A., TAŞYAPI ÇELEBİ A., URHAN O.
International Journal of Electrical and Computer Engineering, 2018 (Scopus)
- II. **FPGA Implementation of Adaptive Clock Recovery for TDMoIP Systems**
DEMİR S., ÇELEBİ A.
International Journal of Electronics and Communication Engineering, vol.12, no.1, pp.11-15, 2018 (Peer-Reviewed Journal)
- III. **Efficient Hardware Architecture for Selective Gray Coded Bit Plane Based Low Complexity Motion Estimation**
Aslam M., ÇELEBİ A.
GAZI UNIVERSITY JOURNAL OF SCIENCE, vol.30, no.1, pp.69-78, 2017 (ESCI)

Refereed Congress / Symposium Publications in Proceedings

- I. **Implementation and Optimization of LeNet-5 Model for Handwritten Digits Recognition on FPGAs using Brevitas and FINN**
Njuguna J. C., Celebi A. T., ÇELEBİ A.
2023 Innovations in Intelligent Systems and Applications Conference, ASYU 2023, Sivas, Turkey, 11 - 13 October 2023
- II. **Multi-Lane Detection System Based on Segmentation Model for Autonomous Vehicles**
Özen S., Kaya U., Semiz A., ÇELEBİ A.
10th International Conference on Electrical and Electronics Engineering, ICEEE 2023, İstanbul, Turkey, 8 - 10 May 2023, pp.18-23
- III. **An FPGA based Real-Time Flat Field Correction for Infrared Focal Plane Arrays**
Njuguna J. C., Alabay E., ÇELEBİ A., Celebi A. T., Gullu M. K.
2023 Innovations in Intelligent Systems and Applications Conference, ASYU 2023, Sivas, Turkey, 11 - 13 October 2023
- IV. **Efficient Hardware Implementation of Real-Time Object Tracking**
Njuguna J. C., Alabay E., ÇELEBİ A., Celebi A., Gullu M. K.
30th Signal Processing and Communications Applications Conference, SIU 2022, Safranbolu, Turkey, 15 - 18 May 2022
- V. **Field programmable gate arrays implementation of two-point non-uniformity correction and bad pixel replacement algorithms**
Njuguna J. C., Alabay E., ÇELEBİ A., Celebi A., Gullu M. K.
2021 International Conference on INnovations in Intelligent SysTems and Applications, INISTA 2021, Kocaeli, Turkey, 25 - 27 August 2021
- VI. **Field programmable gate arrays based multi camera thermal and day vision video processing and distribution system**
Basli A., Kahraman C., Ates B., ÇELEBİ A.
2021 International Conference on INnovations in Intelligent SysTems and Applications, INISTA 2021, Kocaeli, Turkey, 25 - 27 August 2021
- VII. **Field Programmable Logic Arrays Implementation of Scene-Based Nonuniformity Correction Algorithm**
Njuguna J. C., Alabay E., ÇELEBİ A.
8th International Conference on Electrical and Electronics Engineering (ICEEE), Antalya, Turkey, 9 - 11 April 2021, pp.6-9
- VIII. **Implementation of MOSSE Object Tracking Algorithm on FPGA with High Level Synthesis Approach**
Tuncay E., ÇELEBİ A.

12th International Conference on Electrical and Electronics Engineering (ELECO), Bursa, Turkey, 26 - 28 November 2020, pp.241-245

- IX. **Epileptic Seizure Prediction for Imbalanced Datasets**
Cosgun E., ÇELEBİ A., GÜLLÜ M. K.
Medical Technologies Congress (TIPTEKNO), İzmir, Turkey, 3 - 05 October 2019, pp.290-293
- X. **Hardware Implementation of Local Binary Pattern Based Two-Bit Transform Motion Estimation**
YAVUZ S., ÇELEBİ A., TAŞYAPI ÇELEBİ A., URHAN O.
ICMIISP 2018 : 20th International Conference on Medical Imaging, Image and Signal Processing, Thailand, 18 - 19 January 2018, vol.12, pp.19-23
- XI. **Image Histogram Equalizer Hardware Implementation Using High Level Synthesis**
ÖNÜR İ., ÇELEBİ A.
ACADEMICSERA International Conference on Electrical and Electronics Engineering, İstanbul, Turkey, 23 - 24 October 2017, vol.1
- XII. **On The Hardware Implementation of Binarization for High Efficiency Video Coding**
ÇETİN Y., ÇELEBİ A.
International Conference on Electrical and Electronics Engineering, İstanbul, Turkey, 23 - 24 October 2017, vol.1, pp.1-5
- XIII. **Local Binary Pattern Method and its Hardware Architecture for Low-Complexity Motion Estimation**
YAVUZ S., TAŞYAPI ÇELEBİ A., ÇELEBİ A., URHAN O.
25th Signal Processing and Communications Applications Conference (SIU), Antalya, Turkey, 15 - 18 May 2017
- XIV. **Integer 1-Bit Transform Method and its Hardware Architecture for Low-Complexity Block-Based Motion Estimation**
Yavuz S., TAŞYAPI ÇELEBİ A., ÇELEBİ A., URHAN O.
25th Signal Processing and Communications Applications Conference (SIU), Antalya, Turkey, 15 - 18 May 2017
- XV. **Reusable Verification Model for Motion estimation Algorithm**
Aslam M., ÇELEBİ A.
IP SoC 2016 IP-SOC-IOT Conference Exhibition, 5 - 07 December 2016
- XVI. **Hybrid hardware architecture for low complexity motion estimation algorithm**
Aslam M., ÇELEBİ A.
IP SoC 2016 IP-SOC-IOT Conference Exhibition, 5 - 07 December 2016
- XVII. **Model Tabanlı Donanım Geliştirme Ortamları ile RTL Tasarımların Tümüleştirilmesi için Alternatif bir Yöntem**
DEMİR S., HATİPOĞLU E., ÇELEBİ A.
Gömülü Sistemler ve Uygulamaları Sempozyumu, GömSis 2014, İstanbul, Turkey, 4 - 05 December 2014, vol.1, pp.57-60
- XVIII. **KNX Ev Bina Otomasyon Sistemin Kablosuz Algılayıcı Ağı Temelli Bir Yaklaşımla Genişletilmesi**
GÖKBAYRAK A. B., KILIVAN S., ÇELEBİ A., AKIN S., URHAN O.
Gömülü Sistemler ve Uygulamaları Sempozyumu, GömSis 2014, İstanbul, Turkey, 4 - 05 December 2014, vol.1, pp.53-56
- XIX. **Low Bit Depth Representation Based Embedded Motion Estimation Architecture**
Hatipoglu E., ÇELEBİ A.
22nd IEEE Signal Processing and Communications Applications Conference (SIU), Trabzon, Turkey, 23 - 25 April 2014, pp.2158-2161
- XX. **Embedded Empirical Mode Decomposition Intellectual Property Core**
Horasanli N., ÇELEBİ A.
22nd IEEE Signal Processing and Communications Applications Conference (SIU), Trabzon, Turkey, 23 - 25 April 2014, pp.2035-2038
- XXI. **HARDWARE DESIGN OF ANEMBEDDED REAL-TIME ACOUSTIC SOURCE LOCATION DETECTOR**
Turkoglu C., Cagdas S., ÇELEBİ A., ERTÜRK S.
6th IFIP International Conference on New Technologies, Mobility and Security (NTMS), Dubai, United Arab Emirates, 30 March - 02 April 2014

- XXII. **Hierarchical fast bidimensional empirical mode decomposition and its hardware architecture**
SERKAN S., ÇELEBİ A., URHAN O.
2013 21st Signal Processing and Communications Applications Conference (SIU), Haspolat, Turkey, 24 - 26 April 2013, vol.1, pp.1-4
- XXIII. **On the FPGA Implementation Of Empirical Mode Decomposition Algorithm Using FPGA**
Kose I., ÇELEBİ A.
21st Signal Processing and Communications Applications Conference (SIU), CYPRUS, 24 - 26 April 2013
- XXIV. **Panoramik Kamera Sistemi PAN KAM**
TEKYILDIZ A., GÜVENEL Ç., DUVAR R., ÇELEBİ A., URHAN O., ERTÜRK S.
Gömülü Sistemler ve Uygulamaları Sempozyumu (gÖMsİS 2012), İstanbul, Turkey, 29 - 30 November 2012, vol.1, pp.113-114
- XXV. **Mikrodenetleyici Tabanlı Lazer Mesafe Ölçer**
ESEN U., CABBAS O. D., ÇELEBİ A., URHAN O., ERTÜRK S.
Gömülü Sistemler ve Uygulamaları Sempozyumu (GömSis 2012), İstanbul, Turkey, 29 - 30 November 2012, vol.1, pp.95-98
- XXVI. **An uncooled infrared camera with embedded real-time target tracking capability Gerçek zamanlı gömülü hedef takibi yapabilen soğutmasız kızıl ötesi kamera**
Güvenel Ç., Tekyildiz A., Türkoğlu Ç., Yıldız Ç., KÜÇÜKMANİSA A., ÇELEBİ A., URHAN O., GÜLLÜ M. K., ERTÜRK S.
2012 20th Signal Processing and Communications Applications Conference, SIU 2012, Fethiye, Mugla, Turkey, 18 - 20 April 2012
- XXVII. **FPGA implementation of cubic spline interpolation method for empirical mode decomposition**
ÇAĞDAŞ S., ÇELEBİ A.
2012 20th Signal Processing and Communications Applications Conference (SIU), Mugla, Turkey, Turkey, 18 - 20 April 2012, vol.1, pp.1-4
- XXVIII. **IR Camera System with Embedded Target Tracking**
GÜVENEL Ç., TEKYILDIZ A., TÜRKOĞLU Ç., ÇELEBİ A., URHAN O., GÜLLÜ M. K., ERTÜRK S.
2nd Annual International Conference on Advances in Distributed and Parallel Computing (ADPC 2011) - special track on Real-Time and Embedded Systems (RTES 2011), 12 - 13 October 2011, vol.1, pp.71-74
- XXIX. **HIGH PERFORMANCE HARDWARE ARCHITECTURE FOR CONSTRAINED ONE-BIT TRANSFORM BASED MOTION ESTIMATION**
ÇELEBİ A., URHAN O.
19th European Signal Processing Conference (EUSIPCO), Barcelona, Spain, 29 August - 02 September 2011, pp.2151-2155
- XXX. **Efficient Hardware Architecture with Data Reuse and Fast Search Algorithm Support for Low Complexity Motion Estimation**
ÇELEBİ A., ERTÜRK S., LEE H. J.
24th International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC), JEJU, South Korea, 5 - 08 May 2009, vol.1, pp.1-4
- XXXI. **Truncated Gray-Coded Bit-Plane Matching Based Motion Estimation and its Hardware Architecture**
ÇELEBİ A., AKBULUT O., URHAN O., ERTÜRK S.
IEEE 17th Signal Processing and Communications Applications Conference, Antalya, Turkey, 9 - 11 April 2009, pp.906-909
- XXXII. **MVBLA Based Design of Constrained 1-Bit Transform Based Motion Estimation Algorithm**
ÇELEBİ A., URHAN O., Ertuerk S., Hamzaoglu I., Duendar G.
IEEE 16th Signal Processing and Communications Applications Conference, Aydın, Turkey, 20 - 22 April 2008, pp.581-582
- XXXIII. **Implementation of Constrained 1-Bit Transform based motion estimation algorithm with an FPGA based architecture**
ÇELEBİ A., URHAN O., ERTÜRK S., Duendar G.
IEEE 15th Signal Processing and Communications Applications Conference, Eskişehir, Turkey, 11 - 13 June 2007, pp.1005-1006

- XXXIV. **Patlama Riski Bulunan Ortamlarda Kullanılabilecek Kendinden Emniyetli Algılayıcı Tasarımı**
İrkit S., ÇELEBİ A.
ELECO 2016 - Elektrik - Elektronik ve Biyomedikal Mühendisliği Konferansı, Bursa, Turkey, 1 - 03 March 2016, vol.1, pp.207-211
- XXXV. **A 10 bit 500Ms s Two Step Flash ADC**
ÇELEBİ A., AYTAR O., TANGEL A.
IEEE Int. Conf. on Computer as a tool, Belgrade, Serbia And Montenegro, 21 - 24 November 2005, pp.898-901
- XXXVI. **An 8 Bit CMOS Folding ADC Implementation Using TIQ Based Flash ADC Core**
TEKİN M. F., TANGEL A., AYTAR O., ÇELEBİ A.
12th International Mixed Design of Integrated Circuits and Systems Conference (MIXDES 2005), Krakow, Poland, 22 - 25 May 2005, vol.1, pp.43-45
- XXXVII. **TIQ Tabanlı 8 bit Folding A D Dönüştürücü Tasarımı**
TEKİN M. F., TANGEL A., AYTAR O., ÇELEBİ A.
ELECO 2004 Elektrik-Elektronik-Bilgisayar Mühendisliği Sempozyumu, Bursa, Turkey, 8 - 12 December 2004, vol.1, pp.118-122
- XXXVIII. **8-bit 1Gs/s Semi-Flash ADC Based on Threshold Inverter Quantization Technique**
Aytar O., Çelebi A., Tangel A., Tekin F.
MIXDES-2004, Warszawa, Poland, 5 - 07 January 2004
- XXXIX. **A VLSI Implementation for Fast All Boolean Motion Estimation Based on Pre Coded Imag Planes Matching**
Çelebi A., Ertürk S., Tangel A.
3rd International Conference on Electrical and Electronics Engineering (ELECO'2003), Bursa, Turkey, 3 - 07 December 2003, vol.1, pp.36-40

Supported Projects

- Çelebi A., Taşyapı Çelebi A., TUBITAK Project, Derin Öğrenme Kabiliyetli Gömülü İşlem Modülü, 2023 - 2025
- Çelebi A., TUBITAK Project, Sanallaştırma Destekli Genişletilebilir Endüstriyel Ağ Geçidi, 2022 - 2024
- Çelebi A., Taşyapı Çelebi A., TUBITAK Project, LWIR Termal Kamera Tasarımı, 2021 - 2024
- Çelebi A., Taşyapı Çelebi A., TUBITAK Project, FPGA Tabanlı Gömülü İşlem Modülü, 2022 - 2023
- Çelebi A., TUBITAK Project, IoT Destekli Enerji Yönetim Cihaz Ailesi, 2022 - 2023
- Çelebi A., TUBITAK Project, Enerjisini Hattan Alan Yerli Arıza Gösterge Cihazı Geliştirilmesi ve Pilot Uygulaması, 2021 - 2023
- Çelebi A., TUBITAK Project, PORTATİF EMG CİHAZI, 2021 - 2023
- Çelebi A., Taşyapı Çelebi A., TUBITAK Project, ASEYE-ASİS Askeri Çevresel Gözetleme Sistemi, 2020 - 2023
- ÇELEBİ A., COŞGUN E., Project Supported by Higher Education Institutions, EEG İŞARETLERİ KULLANILARAK EPİLEPSİ NÖBET TAHMİN SİSTEMİNİN FPGA TABANLI DONANIM İLE GERÇEKLEŞTİRİLMESİ, 2018 - 2022
- Çelebi A., TUBITAK Project, CAN FD FPGA Donanım Çekirdeğinin Ürünleştirilmesi, 2020 - 2021
- Çelebi A., TUBITAK Project, Video Sinyal İşleme Veriyolu Donanım Çekirdeği, 2019 - 2021
- Çelebi A., Taşyapı Çelebi A., TUBITAK Project, Kızılötesi Video Kamera Çekirdeği, 2019 - 2021
- ÇELEBİ A., Project Supported by Higher Education Institutions, 2019- TÜMLEŞİK SİSTEMLER LABORATUVARI, 2019 - 2020
- Urhan O., Çelebi A., Taşyapı Çelebi A., TUBITAK Project, HEVC için Paralel ve Donanımsal Gerçeklemeye Uygun Hareket Kestirim Yaklaşımları, 2016 - 2018
- Çelebi A., TUBITAK Project, CAN FD Fikri Mülkiyet Çekirdeği, 2015 - 2017
- ÇELEBİ A., TUBITAK Project, JESD204B Fikri Mülkiyet Çekirdeği, 2014 - 2015
- ÇELEBİ A., TUBITAK Project, JESD204B Düşünce İyelik Çekirdeğinin Geliştirilmesi, 2013 - 2015
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